

Form PTO-1449 (REV. 8-83)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 42390.P6643	SERIAL NO. Not yet assigned	
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				APPLICANT Brian D. Possley		
				FILING DATE March 4, 1999	GROUP Not yet assigned	
U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	
<i>HJ</i>	5,591,995	1-7-97	Ching -Hao Shaw	—	—	
<i>JV</i>	5,796,128	8-18-98	Tran, et al.	—	—	
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER <i>Njm</i> ✓ <i>NV</i>			DATE CONSIDERED <i>12-27-99</i>			
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

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	5,055,716	10-8-91	Abbas El Gamel	—	—	—
	5,068,548	11-26-91	Abbas El Gamel	—	—	—
	5,289,021	2-22-94	Abbas El Gamel	—	—	—
	5,341,041	8-23-94	Abbas El Gamel	—	—	—
	5,563,430	10-8-96	Hashimoto, et al.	—	—	—
	5,684,311	11-4-97	Ching-Hao Shaw	—	—	—
	5,780,883	7-14-98	Tran, et al.	—	—	—
	5,789,966	8-4-98	Roland Albert Bechade	—	—	—

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			SiArc Base Array, SPG Central Logic Engineering, Intel Corporation			

EXAMINER

N/A

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